

Application Note AN4006

Phase Coherence: Terminology

Purpose

This application note defines common terms used when referring to Phase Coherent signals.

Introduction

When talking about signals and phase coherence, the vocabulary is sometimes used interchangeably, although each term has a very specific meaning. In this application note, we give illustrated definitions of the following terms: Phase Continuity, Phase Coherence, Phase Coherent Switching, Phase Memory and Phase Matched Outputs. This should help the reader better understand the features and capabilities of Anapico's Multi-Channel Signal Generators (APMS).



Phase Continuity

A signal is said to be phase continuous if, after switching its frequency, the phase of the signal is the same as before the switch occurred. This means that there is no discontinuity in the signal at the frequency switching point. Figure 1 compares a phase continuous signal with a phase discontinuous signal.



Figure 1: Phase Continuity A phase continuous signal doesn't show any discontinuity when switching its frequency.

Single Channel Phase Coherence

As applied to single channel frequency synthesizers, phase coherence describes the relation of the frequency standard to the output frequency. If the output frequency accurately reproduces the relative frequency stability of the standard, the device is considered coherent.

Phase Coherence Between Two Channels

If the phase relationship of two signals remains constant, those two signals are considered to be phase coherent. This means that their relative phase $\Delta \phi$ stays constant over time.



A stricter definition only considers phase coherence between continuous wave (CW) signals of equal frequencies or when their frequencies are multiples of each other.

Application note AN4008 gives an in depth view of phase coherent signals.

Phase Coherent Switching

When referring to the switching behavior of a signal, the term "phase coherence" defines the state of the signal's phase once the switching process is completed.

In order to illustrate this, let us consider two phase coherent signals, Signal 1 and Signal 2, at frequency f_1 with a relative phase $\Delta \phi$. If Signal 2 is switched to frequency f_2 and then back to frequency f_1 , the relative phase between the two signals will be $\Delta \phi$ again, if they are phase coherently switched.



Figure 2: Phase Coherent Switching

The relative phase between signals is maintained when they have the same frequency. When the frequency is changed, there is a phase discontinuity only on the switched signal.



Phase Memory

A signal is said to have phase memory if, when it is switched from frequency f_1 to frequency f_2 and then back to frequency f_1 , the signal's phase resumes at the position it would have had if it had run continuously at frequency f_1 . In other words, whenever the signal goes back to a previously set frequency, it behaves as if it had been running continuously at said frequency the whole time. Phase memory usually implies phase discontinuity; this is shown in Figure 3



Figure 3: Phase Memory

Phase Matched Outputs

A multi-channel signal generator is said to have phase matched signals if the outputs are aligned to each other to have a calibrated 0 degree relative phase at all output frequencies. Anapico's multi-channel signal generators outputs show good phase matching even without initial calibration. Optional calibration allows high degree of phase matching.





Conclusion

We have shown that there is a well defined terminology to describe the different characteristics of phase coherent signals. This terminology is sometimes used interchangeably; however it is important to understand the subtle differences that exist between the described phenomenons. Anapico's APMS multi-channel signal generators are able to address those requirements with high precision and stability. Thanks to its unique design, the APMS series provides outstanding channel-to-channel Phase Coherence; with the addition of Option PHS, the generators feature Phase Coherent Switching as well as Phase Memory and Phase Matched Outputs.